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EXAMINER

PATEL, ASHOKKUMAR B

ART UNIT PAPER NUMBER

2154

DATE MAILED: 09/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/941,254

Applicant(s)

HOCHMUTH ET AL.

Examiner

Ashok B. Patel

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 13 April 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Other: \_\_\_\_\_

**DETAILED ACTION**

1. Claims 1-20 are subject to examination.

***Response to Arguments***

2. Applicant's arguments with respect to claims 1-20 have been considered but are moot in view of the new ground(s) of rejection.

***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –  
(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Bialik et al. (hereinafter Bialik) (US 2005/0076134 A1).

**Referring to claim 1,**

Bialik teaches an apparatus for communicating graphics between at least two remotely-located computers across a computer network (Figs. 3, 4 and 5) comprising:

an input for receiving a video signal output from a graphics card of a source computer (Fig.3, element 32, Fig. 4; elements 66, 68, Fig. 5, element 76);

a memory for storing discrete units of the video signal (Fig.3, element 32, Fig. 4; elements 46, 47);

a compression circuit for compressing a plurality of the discrete units into a compressed video signal (Fig.3, element 10, Fig. 4, element 41, Fig. 5, element 78);

a network interface circuit coupled to both the compression circuit and the computer network, the network interface circuit configured to format and communicate the compressed video signal over the computer network to a remote computer; and an output coupled to the computer network (Fig. 3, element 30, Fig. 4, element 48, Fig.5, element 73).

**Referring to claim 2,**

Bialik teaches an apparatus for communicating graphics across a computer network (Figs. 3, 4 and 5) comprising:

an input for receiving a video signal (Fig.3, element 32, Fig. 4; elements 66, 68, Fig. 5, element 76);

a memory for storing discrete units of the video signal (Fig.3, element 32, Fig. 4; elements 46, 47);

a compression circuit for compressing a plurality of the discrete units into a compressed video signal (Fig.3, element 10, Fig. 4, element 41, Fig. 5, element 78);

and

a network interface circuit coupled to both the compression circuit and the computer network, the network interface circuit configured to format and communicate the compressed video signal over the computer network to a remote computer. (Fig. 3, element 30, Fig. 4, element 48, Fig.5, element 73).

**Referring to claim 3,**

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Bialik teaches the apparatus of claim 2, wherein the video signal is in compliance with a Digital Visual Interface (DVI) standard (Fig.3, element 23).

**Referring to claim 4,**

Bialik teaches the apparatus of claim 2, wherein the video signal is an analog video signal (Fig.3, element 23).

**Referring to claim 5,**

Bialik teaches the apparatus of claim 2, further comprising a circuit for converting an analog video signal into a digital video signal. (page 1, para.[0017], Fig. 3, elements 23 and 21)

**Referring to claims 6 and 7,**

Bialik teaches the apparatus of claim 2, wherein the computer network comprises a local area network (LAN), and apparatus of claim 2, wherein the computer network comprises a wide area network (WAN). ( page 3, para.[0069], "a. Receiving input streams 26 from a local or wide area networks and feeding them into the MFM.nd [0070] b. Receiving 21 compressed streams from the MFM and transmitting them to local or wide area distribution networks.")

**Referring to claim 8,**

Bialik teaches the apparatus of claim 2, wherein the network interface circuit is configured to format the compressed video signal into a plurality of Internet Protocol (IP) packets that are communicated over the computer network to the remote computer. (Page 3, para.[0068]-[0070])

**Referring to claim 9,**

Bialik teaches the apparatus of claim 2, further comprising a second input for receiving a second video signal (Fig. 3, element 23).

**Referring to claim 10,**

Bialik teaches the apparatus of claim 9, wherein the compression circuit is further configured to separately compress a plurality of discrete units for each of the video signals. (Fig.3, element 10, page 2, para.[0061]-[0063]).

**Referring to claim 11,**

Bialik teaches the apparatus of claim 2, wherein the network interface circuit is configured to format and communicate separately compressed video signals to different remote computers, such that a first remote computer receives a first compressed video signal and a second remote computer receives a second compressed video signal. (Fig.3, element 30 and 26, Page 3, para.[0068]-[0070])

**Referring to claim 12,**

Bialik teaches the apparatus of claim 2, further comprising a plurality of network interface circuits, each network interface circuit being coupled to both a compression circuit and the computer network, each network interface circuit being configured to format and communicate the compressed video signal over the computer network to a remote computer. (Fig.3, element 30 and 26, Page 3, para.[0068]-[0070], Fig.3, element 10, page 2, para.[0061]-[0063], Figs. 4 and 5).

**Referring to claims 13 and 14,**

Bialik teaches the apparatus of claim 2, wherein the apparatus comprises a connector for direct connection to a source computer that supplies the video signal, wherein the

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connector comprises signals carrying power signals for powering the apparatus, and apparatus of claim 13, wherein the connector is an edge connector configured to directly plug into a card slot of a motherboard of the source computer. (Fig. 3, elements 10, 30, 23 21, 26)

**Referring to claim 15,**

Bialik teaches the apparatus for communicating graphics across a computer network (Figs. 3, 4 and 5) comprising: an input for receiving a video signal (Fig. 3, element 32, Fig. 4, elements 66, 68, Fig. 5, element 76); and a network interface circuit coupled to both the input and the computer network, the network interface circuit configured to format and communicate the video signal over the computer network to a remote computer (Fig. 3, element 30, Fig. 4, element 48, Fig. 5, element 73).

**Referring to claim 16,**

Bialik teaches the apparatus of claim 15, wherein the network interface circuit is configured to format the video signal into a plurality of Internet Protocol (IP) packets that are communicated over the computer network to the remote computer (page 1, para.[0017], Fig. 3, elements 23 and 21).

**Referring to claim 17,**

Bialik teaches the apparatus of claim 15, further comprising a second input for receiving a second video signal. (Fig. 3, element 23).

**Referring to claim 18,**

Bialik teaches the apparatus of claim 15, wherein the network interface circuit is configured to separately format and communicate each received video signal to

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different remote computers, such that a first remote computer receives the a first video signal and a second remote computer receives a second video signal (Fig.3, element 30 and 26, Page 3, para.[0068]-[0070]).

**Referring to claim 19,**

Bialik teaches the method for communicating graphics across a computer network (Figs. 3, 4 and 5) comprising: receiving a video signal (Fig. 3, element 32, Fig. 4, elements 66, 68, Fig. 5, element 76); converting the video signal into a format suitable for communication over a computer network; and communicating the converted video signal across the computer network to a remote computer(Fig. 3, element 30, Fig. 4, element 48, Fig. 5, element 73).

**Referring to claim 20,**

Bialik teaches the method of claim 19, wherein the step of converting comprises forming a plurality of Internet Protocol (IP) packets collectively embodying the video signal. (page 1, para.[0017], Fig. 3, elements 23 and 21).

***Conclusion***

**Examiner's note:** Examiner has cited particular columns and line numbers in the references as applied to the claims above for the convenience of the applicant.

Although the specified citations are representative of the teachings of the art and are applied to the specific limitations within the individual claim, other passages and figures may apply as well. It is respectfully requested from the applicant in preparing responses, to fully consider the references in entirety as potentially teaching all or part of the



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claimed invention, as well as the context of the passage as taught by the prior art or disclosed by the Examiner.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ashok B. Patel whose telephone number is (571) 272-3972. The examiner can normally be reached on 8:00am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John A. Follansbee can be reached on (571) 272-3964. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Abp  
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